

FIG. 3

FIG. 3A FIG. 3B

FIG. 3A

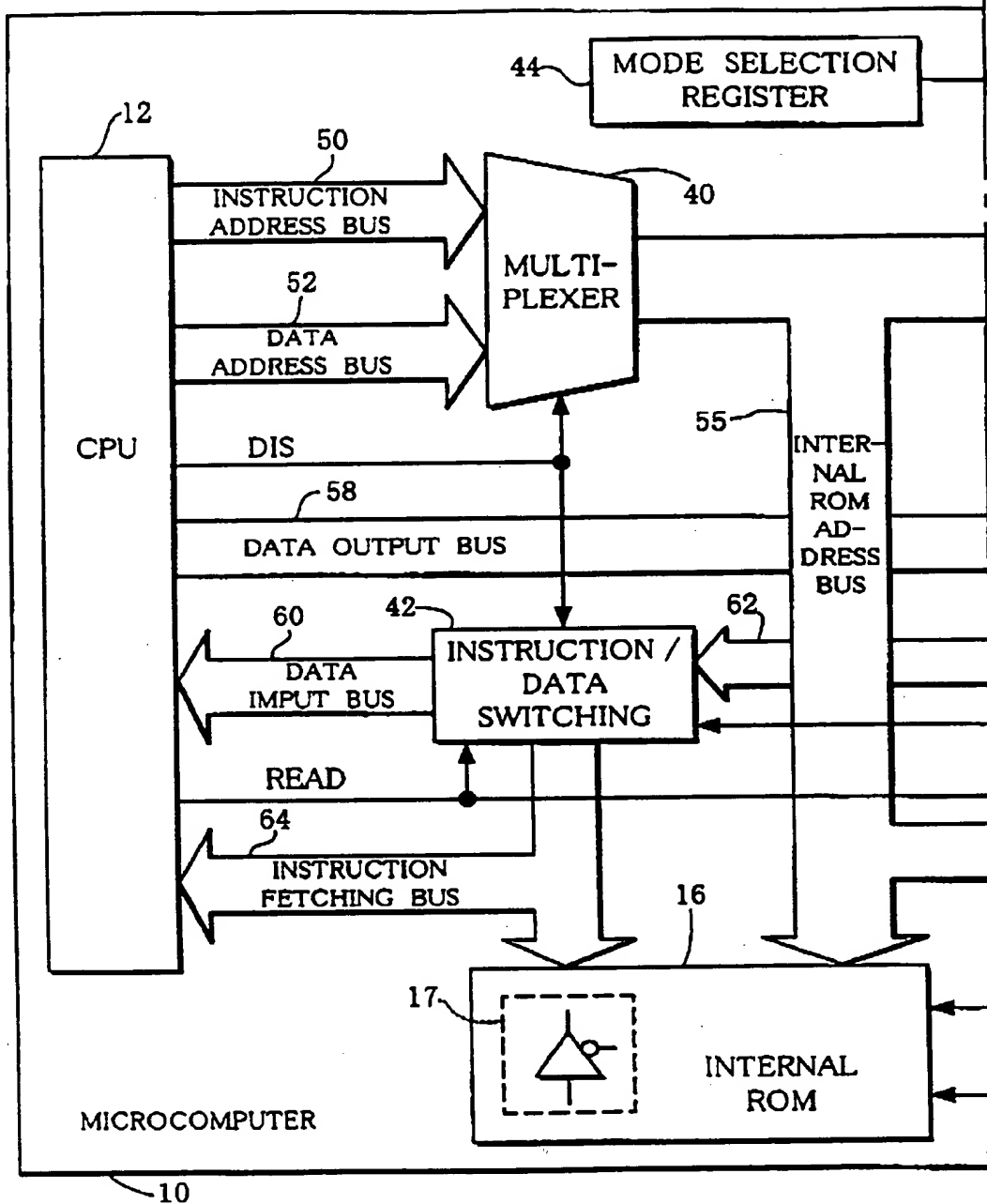


FIG. 3B

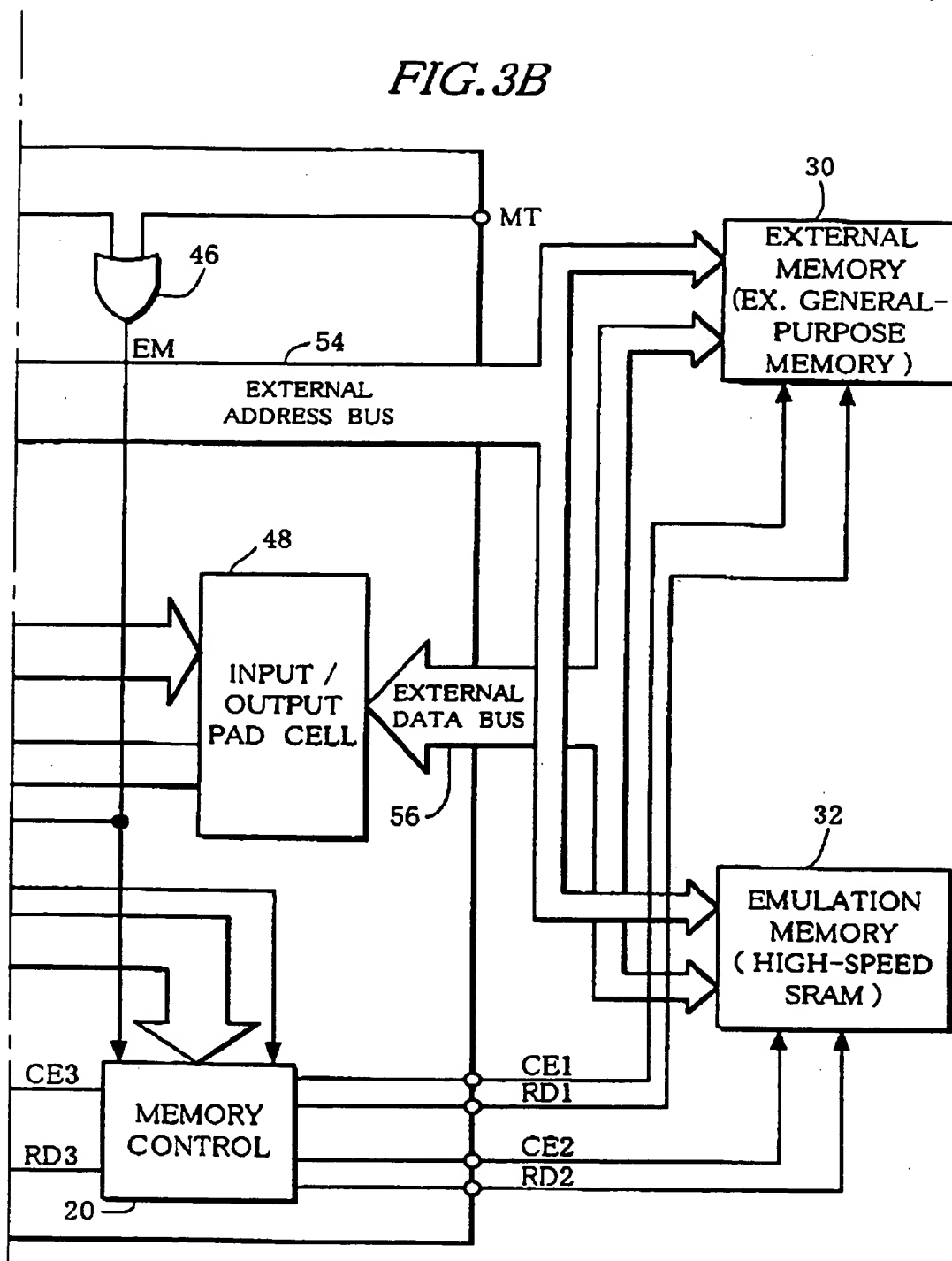


FIG. 4

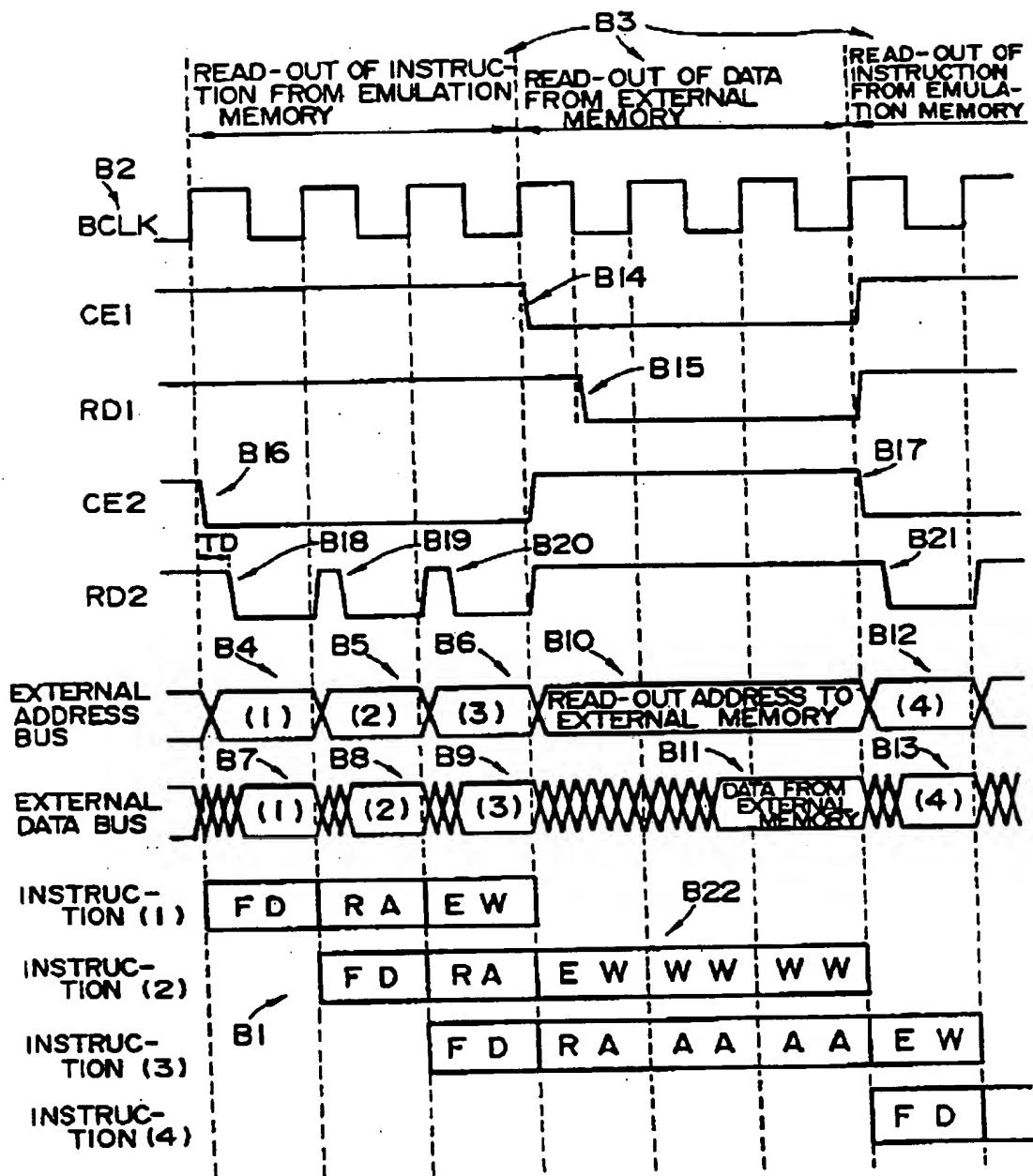


FIG.5

TERMINAL MT	
0 0	BOOTING FROM EMULATION MEMORY
0 1	BOOTING FROM EMULATION MEMORY AFTER INFORMATION HAS BEEN TRANSFERRED TO EMULATION MEMORY (OTP MODE)
1 0	BOOTING FROM INTERNAL ROM
1 1	BOOTING FROM EXTERNAL MEMORY

FIG. 6

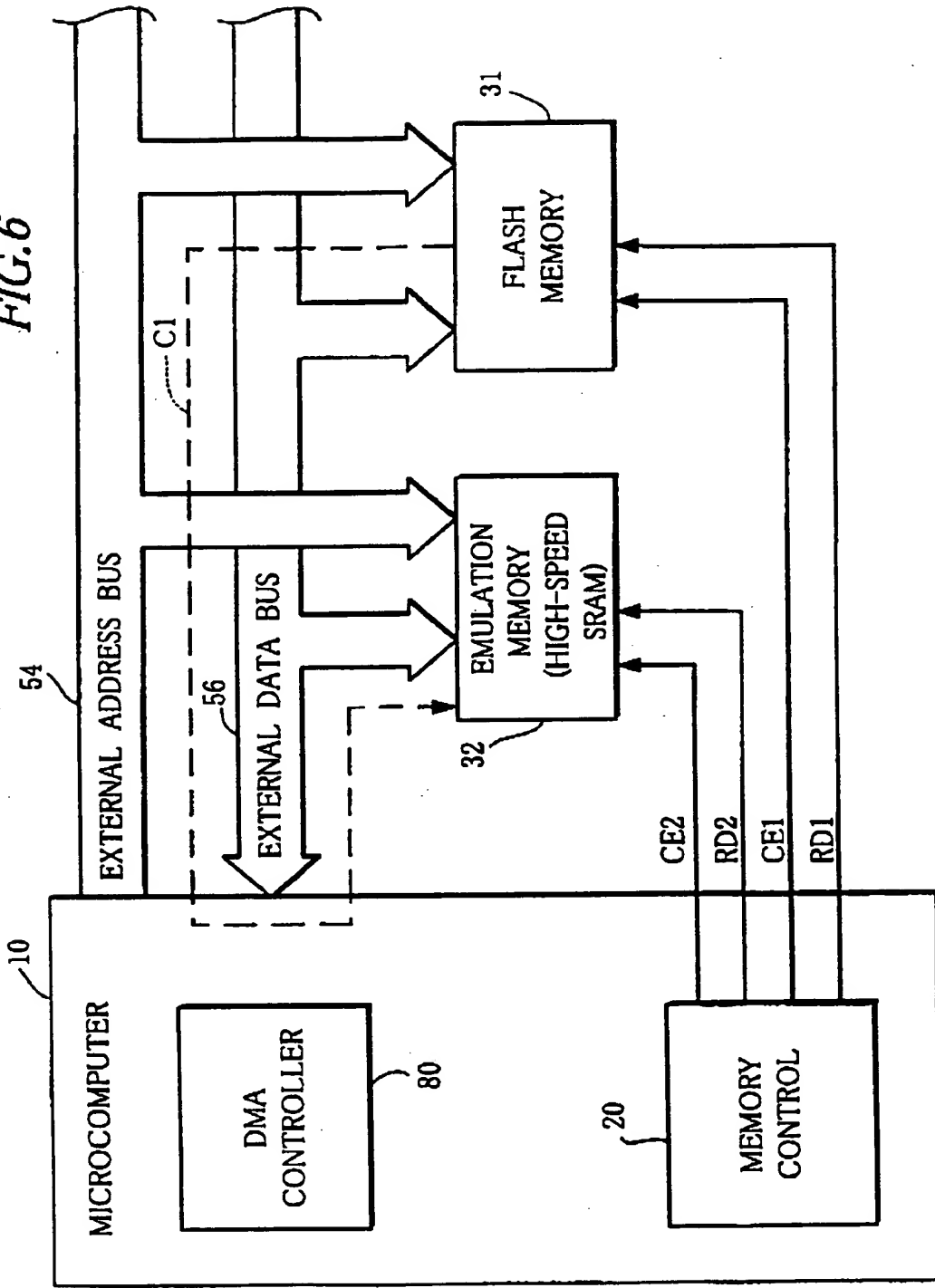
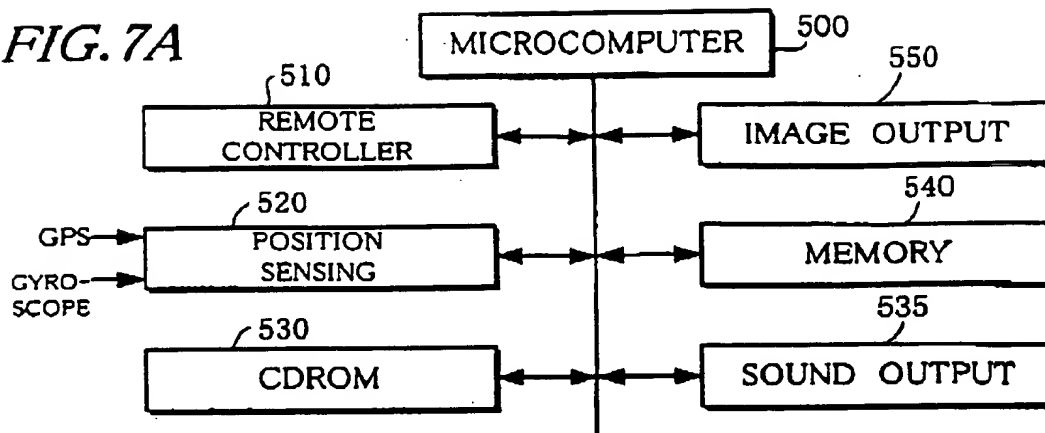
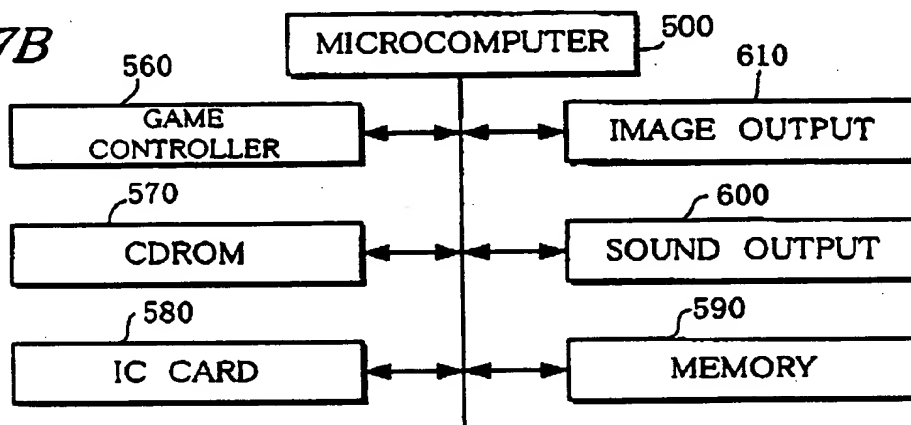


FIG. 7A



**FIG. 7B**



**FIG. 7C**

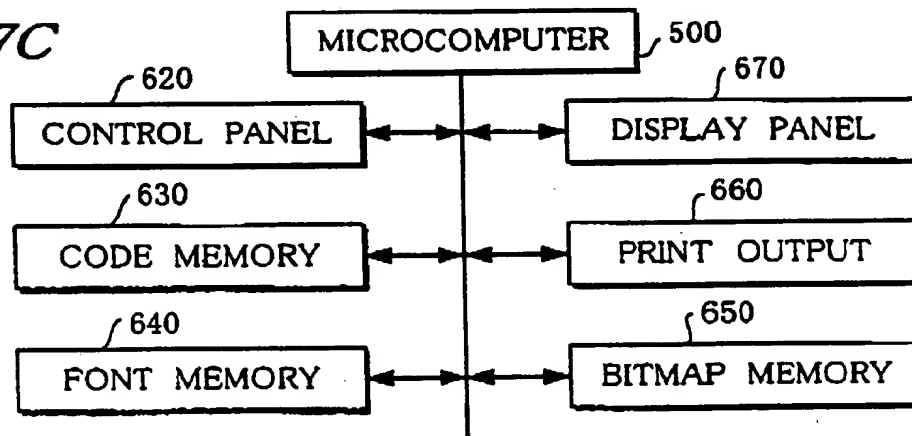




FIG. 8A

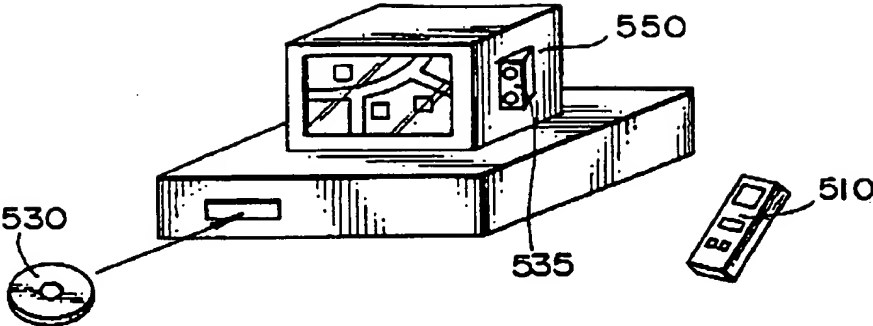


FIG. 8B

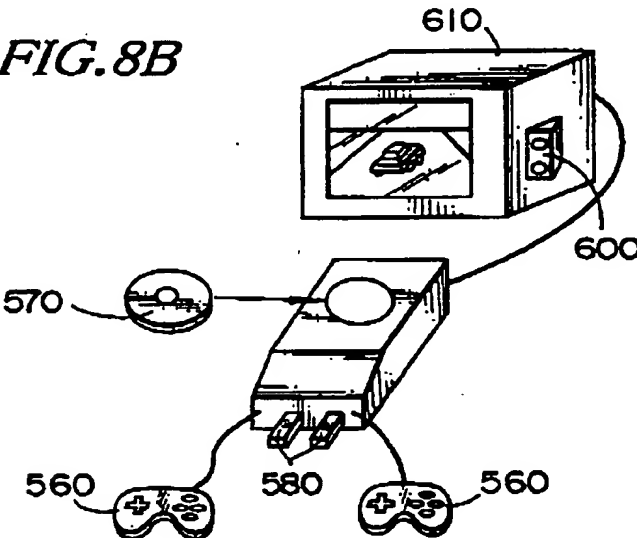


FIG. 8C

